

Units: Mikron

Lage	Lagenaufbau	Impedanz ID	verpresste Dicke	Hersteller	Herstellerbezeichnung	Tg	Beschreibung	Type	εr
1		1, 2, 3, 4	36,000	Polar Samples	SM/001		Liquid Photolmageable Mask	SolderMask	3,500
			220,500	Panasonic	HIPER R-1650M	153,000	PrePreg 7628/53	FR-4	4,400
			220,500	Panasonic	HIPER R-1650M	153,000	PrePreg 7628/53	FR-4	4,400
			35,000						
2			700,000	Panasonic	HIPER R-1755M	153,000	Core	FR4	4,700
			35,000						
3			220,500	Panasonic	HIPER R-1650M	153,000	PrePreg 7628/53	FR-4	4,400
			220,500	Panasonic	HIPER R-1650M	153,000	PrePreg 7628/53	FR-4	4,400
4			36,000	Polar Samples	FO/001		Copper Foil	Copper	
				Polar Samples	SM/001		Liquid Photolmageable Mask	SolderMask	4,000

Strukturbild	Impedanz ID	Struktur-Name	Impedanz Signallage	Ref. Lage 1 in Lage	berechnete Impedanz	Zielimpedanz	Tol (+/- %)	Untere Leiterbreite (W1)	Leiterbahn-Separation (S1)	Substrat 1 Dicke (H1)
	1	Coated Microstrip 1B	1	2	99,930	100,000	10,000	145,000	0,000	441,000
	2	Coated Microstrip 1B	1	2	50,000	100,000	10,000	790,000	0,000	441,000
	3	Edge Coupled Coated Microstrip 1B	1	2	99,990	100,000	10,000	280,000	150,000	441,000
	4	Edge Coupled Coated Microstrip 1B	1	2	50,160	50,000	10,000	1450,000	125,000	441,000

Bohr-Bild	1. Lage	2. Lage	Bohrtype
	1	4	Mechanisch PTH

Hinweise

Aufbau-Name: Master	Version:	Revision:	Modifikation:	Änderungsdatum:	Editor	Seite 1/1	
Datum:	Weitere Dokumente:						
Autor:							
Abteilung:							
Standort:							